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CMOS - Wikipedia

Very large-scale
integration (VLSI) is the
process of creating an
integrated circuit (IC)
by combining millions
of MOS transistors onto
a single chip. VLSI
began in the 1970s
when MOS integrated

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circuit chips were widely adopted, enabling complex semiconductor and telecommunication technologies to be developed. The microprocessor and memory chips are VLSI devices.

Very Large Scale Integration - Wikipedia

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Dec. 5: The answers to the previous Exam 4 in 2009 is posted here.

Nov. 26: For those of you who would like to view the cross section of layout design using L-Edit, I have created the .xst that can be used. To view the cross section in L-Edit, go to Tools / Cross-Section, then download and use ECE321_Project.xst file

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Complementary metal-
oxide-semiconductor
(CMOS), also known as
complementary-
symmetry metal-oxide

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-semiconductor (COS-MOS), is a type of MOSFET (metal-oxide-semiconductor field-effect transistor) fabrication process that uses complementary and symmetrical pairs of p-type and n-type MOSFETs for logic functions. CMOS technology is used for constructing integrated circuit (IC) chips ...

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In order to derive

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maximum learning experience, the users are advised to first read the instructions for conducting the labs. There are 'step-by-step' instructions available in each lab to assist the users.

Textbooks List - Solutions Manual

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